

Type TD Subminiature LCD Display Switch

INSTRUCTIONS MANUAL

Rev. 3 December 2007.

Sunmulon Co., Ltd.

<SPECIFICATIONS>

	Contact	Gold plated silver contac	t								
	Electrical Rating	DC12V 100mA (Resistiv	DC12V 100mA (Resistive) ※Minimum DC5V 1mA (Resistive)								
	Insulation Resistance	More than 100MΩ at 500V DC									
Switch	Dielectric Strength	300V AC RMS between I 1000V AC RMS between 50/60Hz for 60sec. At no	terminals and g	ground (switch)	,						
	Mechanical Life	More than 1,000,000 operations More than 3,000,000 operations (push type C)									
	Electrical Life	More than 200,000 operations at max. rated load									
	Very little current loaded	More than 500,000 opera		0							
	Operation Fo	orce (MAX)	2.5N	Total Tr	avel(MAX)	3.0mm					
1.05	Display	FSTN type LCD (1/6 bias 1/25 duty)		Dot Area	5.98mm×13.5mm	l					
LCD	Display capacity	20×52 dots (1,040dots)		Display Data	Anti-synchronous	latch type					
	Dot Size	0.24mm×0.28mm		Display Mode	Graphics/Characte	Graphics/Characters					
Ambier	nt Temperature			−10°C~40°C							
Ambier	nt Humidity			80%RH(max.)		80%RH(max.)					

<ELECTRICAL&OPTICAL CHARACTERISTICS>

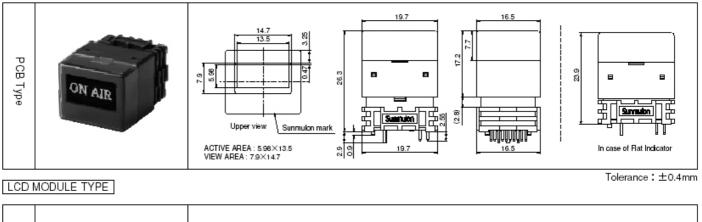
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	
Operating Voltage	VDD	-	+4.75	+5	+5.25	V	
Hi-level Input Voltage	VIH	_	$0.8V_{DD}$	1	-	V	
Low-level Input Voltage	VIL	-	-	I	$0.2 V_{DD}$	V	
Driver IC Operating Current		Display Checkerd Pattern	-	50	105	uA	
Most visible point	6 o'clock point						
Visible Deemee	θ Note 1)	Ta 25 °C	$-50 \sim +60$ d				
Visible Degrees	⊕ Note 2)	Ta 25 °C		$\begin{array}{c ccccc} +5 & +5.25 \\ - & - & - \\ \hline - & 0.2 V_{DD} \\ 50 & 105 \\ \hline \\ ck \text{ point} \end{array}$	deg		
Contrast Ratio	C.R.	Ta 25 ℃	-	25	-	-	
D	tf	Ta 25 °C	-	200	250	ms	
Response Time	tr	Ta 25 ℃		200	250	ms	

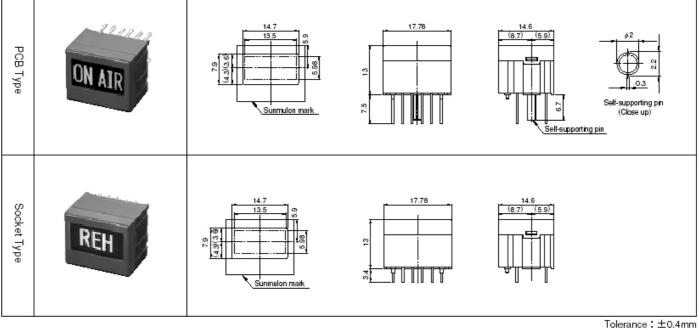
Note 1) θ ; Up / Down direction Note 2) ϕ ; Left / Right direction

LCD Driving Circuit: New Japan Radio Co., LTD (JRC)LCD Control Driver IC NJU6673 (built-in Booster Circuits) Selectable Duty and Bias Ratio: 1/25 Duty, 1/6 Bias Frame frequency (Ff): TYP76Hz (Min.62Hz,Max.90Hz) Serial Interface (SI, SCL, A0, CS) Asynchronous Lutch Type

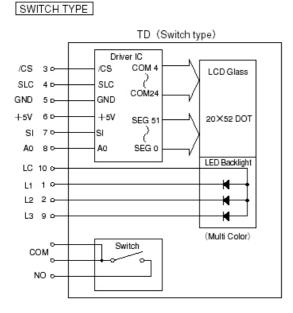
<DIMENSIONS>

SWITCH TYPE

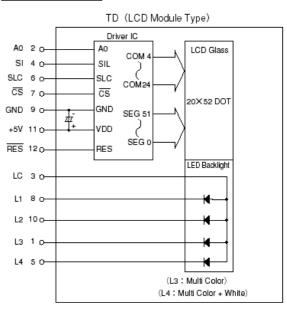




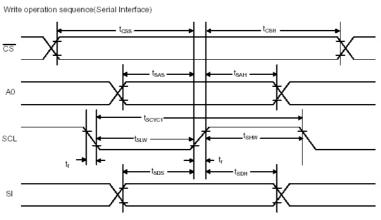
<INTERNAL CURCUIT>



LCD MODULE TYPE



<WRITE OPERATION SEQUENCE>

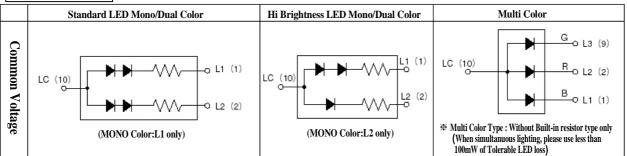


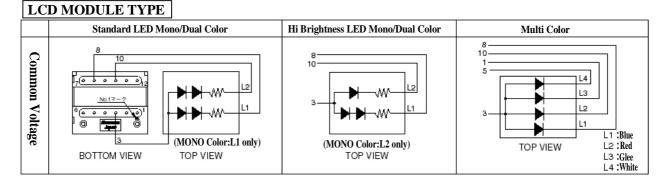
			()	VDD=2.4V-	3.6V, Ta=	-20 to 75°	°C)
PARAMETER	SYMBOL	SIGNAL	CONDITION	MIN	TYP	MAX	UNIT
Serial Clock cycle	t _{scvc}			1000			
SCL "H" Pulse width	t _{snw}	SCL		300			1
SCL "L" Pulse width	t _{stw}			300			
Address Setup Time	t _{sas}	A0		250			
Address Hold Time	t _{sah}	70		400			
Data Setup Time	t _{sps}	SI		250			ns
Data Hold Time	t _{sDH}	5		100			
CS-SCL Time	t _{css}	CS		60			
CO-OCE TIME	t _{CSH}			800			
Rise time, Fall Time	t _r , t _r	CS, SCL SI, A0				15	

Note 1) All timing are based on 20% and 80% of V_{DD} voltage level.

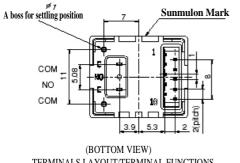
<INTERNAL CONNECTION ARRANGEMENTS FOR BACKLIGHT LED>







<TERMINALS LAYOUT/TERMINAL FUNCTIONS> SWITCH TYPE



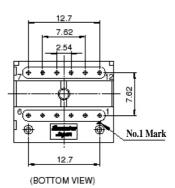
TERMINALS LAYOUT/TERMINAL FUNCTIONS

*Indicator has no COM terminal.

TERMINAL No.	SIGNAL	FUNCTION
1	L1	LED1 Cathode
2	L2	LED2 Cathode
3	/CS	Chip Select
4	SLC	Serial Clock
5	GND	- Power Supply Terminal (Basic Potential)
6	+5V	+ Power Supply Input Terminal (+5V)
7	SI	Serial Data
8	A0	COMMAND/DATA Change
9	L3	LED3 Cathode
10	LC	LED Anode

*Switch Type has no functions of L4 and RES.

LCD MODULE TYPE

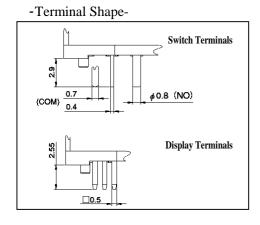


TERMINAL No.	SIGNAL	FUNCTION
1	L3	LED3 Cathode
2	A0	COMMAND/DATA Change
3	LC	LED Anode
4	SI	Serial Data
5	L4	LED4 Cathode
6	SLC	Serial Clock
7	/CS	Chip Select
8	L1	LED1 Cathode
9	GND	- Power Supply Terminal (Basic Potential)
10	L2	LED2 Cathode
11	+5V	+ Power Supply Input Terminal (+5V)
12	/RES	RESET

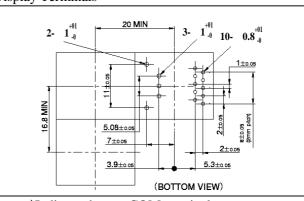
Caution: It may damage the Driver IC, if you input more than +DC5V to VDD+5V terminal. Please never input overdue voltage.

<TERMINAL SHAPE/PCB HOLE DIMENSIONS>

SWITCH TYPE

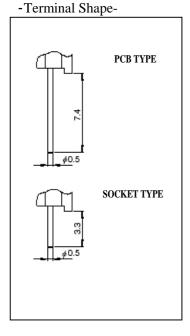


-Display Terminals-

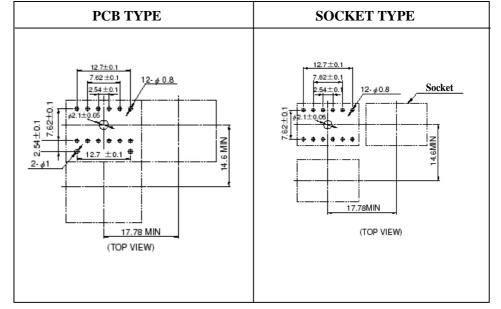


*Indicator has no COM terminal.

LCD MODULE TYPE



-PCB Hole Dimensions-



<Setting up the driver IC> (Please also see the driver IC data book.)

- Setting up the page address

The high order 4 dots (D0 - D3) of "Page 0" and "Page 3" of the page address are not wired to the LCD panel. Send dummy data or change "Page address setting" when sending data.

Lines (column addresses) will be incremented (+1) automatically after every 8-bit.

- Setting up the segment output

Only 52 dots in S0 – S51 are used for segment output, and S52 – S99 are not wired to the LCD panel. Send dummy data or change "Page address setting" to a different start line after sending 52 dots of segment data. In addition, the output terminals of driver IC and LCD glass terminals are assigned in reverse order as shown in the table below. Set up "D0 = 1 : invert" in "Select ADC" of the initial setting commands.

driver IC	LCD	driver IC	LCD	driver IC	LCD	driver IC	LCD	driver IC	LCD
S 0	SEG52	S10	SEG42	S20	SEG32	S30	SEG22	S40	SEG12
S 1	SEG51	S11	SEG41	S21	SEG31	S31	SEG21	S41	SEG11
S2	SEG50	S12	SEG40	S22	SEG30	S32	SEG20	S42	SEG10
S 3	SEG49	S13	SEG39	S23	SEG29	S 33	SEG19	S43	SEG9
S4	SEG48	S14	SEG38	S24	SEG28	S34	SEG18	S44	SEG8
S5	SEG47	S15	SEG37	S25	SEG27	S35	SEG17	S45	SEG7
S 6	SEG46	S16	SEG36	S26	SEG26	S36	SEG16	S46	SEG6
S 7	SEG45	S17	SEG35	S27	SEG25	S37	SEG15	S47	SEG5
S 8	SEG44	S18	SEG34	S28	SEG24	S38	SEG14	S48	SEG4
S 9	SEG43	S19	SEG33	S29	SEG23	S39	SEG13	S49	SEG3

<Table of corresponding segments>

driver IC	LCD	driver IC	LCD	driver IC	LCD	driver IC	LCD	driver IC	LCD
S50	SEG2	S60	NC	S70	NC	S 80	NC	S90	NC
S51	SEG1	S61	NC	S71	NC	S81	NC	S91	NC
S52	NC	S62	NC	S72	NC	S82	NC	S92	NC
S53	NC	S63	NC	S73	NC	S83	NC	S93	NC
S54	NC	S64	NC	S74	NC	S 84	NC	S 94	NC
S55	NC	S65	NC	S75	NC	S85	NC	S95	NC
S56	NC	S66	NC	S 76	NC	S 86	NC	S96	NC
S57	NC	S67	NC	S77	NC	S 87	NC	S97	NC
S58	NC	S68	NC	S78	NC	S88	NC	S98	NC
S59	NC	S69	NC	S 79	NC	S 89	NC	S99	NC

* NC = No circuit

- (j) Setting up the electronic volume register

The reference voltage setting at shipment is at the center of the setting range. If there is no special need to change the setting, set it to "89h" (10001001).

- (n) Built-in power supply ON/OFF

TD types drive LCD using the built-in boosting power supply (power supply for LCD operation) in the driver IC. Set up "D0 = 1 : built-in power supply ON."

- (g) Reading the display data

TD types have serial wiring control and they cannot read the display data.

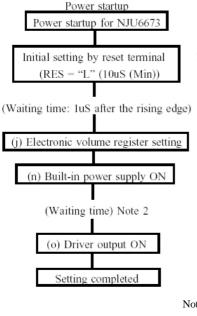
- Other settings

- (a) Display ON/OFF \rightarrow D0 = 1 : ON
- (h) Display without/with highlighting $\rightarrow D0 = 0$: without highlighting (negative display)
- (i) Static drive ON/OFF \rightarrow D0 = 0 : Normal lighting
- (m) Reset \rightarrow "Power on Reset circuit" is configured by internal circuit. It is automatically initialized at power startup. It is also possible to initialize RES terminal intentionally by dropping it to GND.

* There is no RES terminal in switch types.

(o) Driver output $ON/OFF \rightarrow D0 = 1 : ON$

<Initialization setting example>



 Note 1
 Note 1) - Initial setting status

 (1)
 Display OFF

 (2)
 Display without highlighting

 (3)
 Select ADC: Display without highlighting (ADC instruction D₀ = "0")

 (4)
 Read-modify-write off

 (5)
 Built-in power supply OFF

 (6)
 Register data in serial interface cleared

 (7)
 Driver output OFF

 (8)
 Display start line address set to 00_H

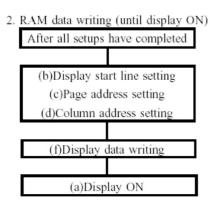
 (9)
 Column address counter set to 00_H

 (10)
 Page address register set to 0 page

 (11)
 Electronic volume register set to 0_H

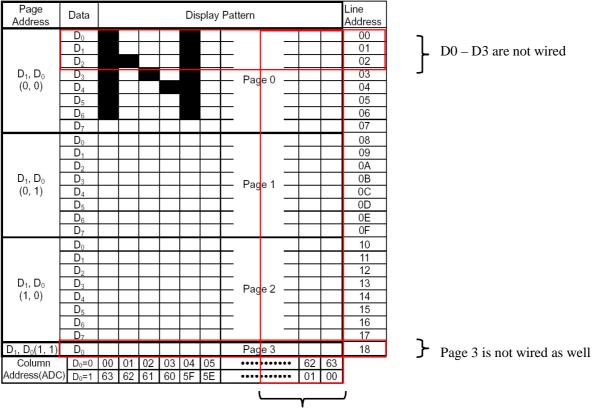
Note 2) Time for built-in power supply to become stable. While 100ms is given in the reference program

"Targetssample," it should cause no problem if it is set to 50ms or longer.



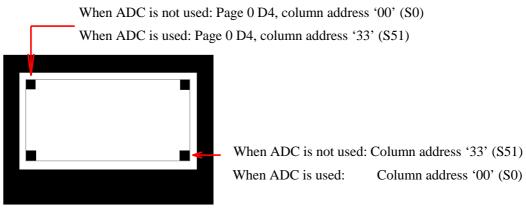
* For settings other than the above, see the driver IC data book and reference program "Targetsample."

<About address map> Please also see description in "void td_puts (unsigned char *dat)" of "main.C" in reference program "Targetsample."



S52 - S99 of segment output is not wired

- The display area for TD types is Page 0 D4 D7 + Page 1 + Page 2 S0 S51.
- When "ADC (highlighted output)" is not used, the column address is '00' \rightarrow '33.' When "ADC (highlighted output)" is used, the address is '33' \rightarrow '00.'



Sunmulon engraving side

- Send dummy data or set "display start line address" in 'D4' in the unconnected display areas of Page 0 D0 D3.
- Send dummy data to the unconnected display areas of S52 S99 segment output or switch the page for each 52 dots transmitted.

<Backlight LED setting>

- Backlight LED has an independent circuit configuration from the LCD driver control circuit.
- When "there is no resistor," provide a LED protection resistor based on "backlight LED setting value."

- Note: Internal temperature increase may reach high in successive installation or concentrated installation. Please select "no resistor" and use an external resistor.
- To use for pulse lighting, set up with reference to "pulse lighting setup conditions."
- Only "no resistor" type can be selected for multi-color types.

Built-in resistor typ	es	Rated cu	rrent val	ue for ba	acklight	LED	
		R	lated curr	ent (mA))		
Rated voltage	Sta	andard typ	be	High brightness type			
(V)	Red	Green	Yellow	Blue	Super	White	
					Green		
5V, 12V(±)5%	3.6	11.7	10.6	10.2	4.1	12	

Note) To use at 24V, use "no resistor" type with external resistor.

Without Built-in resistor types LED rating table (Calculate the external resistance value with reference to below.)

Item	Standard type			High l	orightnes	ss type	Multi-color, multi-color + white			
	Com	Common voltage			Common voltage			Commo	n voltage	e
	Red	Green	Yellow	Blue	Super	White	Red	Blue	Super	White
					Green				Green	
Max. operation current IFM (mA)	50	30	50	20	40	20	30	20	20	20
Reverse DC voltage VR (V)	5	5	5	-	-	-	5	-	-	-
Forward voltage VF (V) Standard value	4	4.2	4.2	2.8	3	3.3	1.85	2.8	3	3.3
Recommended operation current IF (mA)	3.6	11.7	10.6	10.2	4.1	12	9.2	10.2	6	12
Wiring diagram	Mono color (Fig.1), Mono color (Fig.1), Fig.3			Fig.4						
	Dua	al colors (Fi	g.2)	Dua	Dual colors (Fig.2)					

* There is no blue switch type.

- Current reduction rate at Ta = 25°C or higher: 0.33mA/°C (DC)

> : 1.6mA/°C (PULSE)

Pulse lighting setup conditions

- Operating conditions for pulse lighting $(Ta = 25^{\circ}C)$

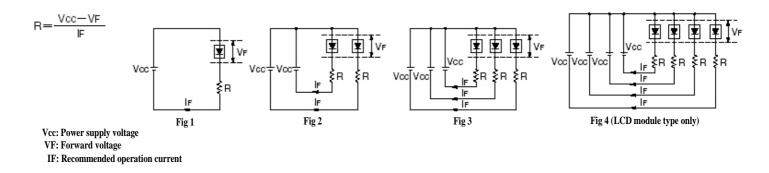
Common for each color	IFM = 100 mA with pulse width Pw = 100	μ S and duty ratio DR = 10^{-1}
- Tolerable LED loss (mW)	Standard type (red, green, yellow)	50mW for each color
(per chip)	High brightness type (blue, Super green)	80mW for each color
	High brightness type (white)	60mW
	Multi-color type (blue, green, red)	80mW for each color

* It is 105mW or lower when 2 or more colors are lit with multi-color type.

Dual color emission combinations (common for switch type and LCD module type) (common for each voltage)

	Standard type High brightness type				
Between LC(+)–L1	Red	Green	Yellow	Yellow	Red
Between LC(+)–L2	Green	Yellow	Super	White	Super
			Green		Green

Reference calculation diagram for external protective resistor



Multi-color + W type

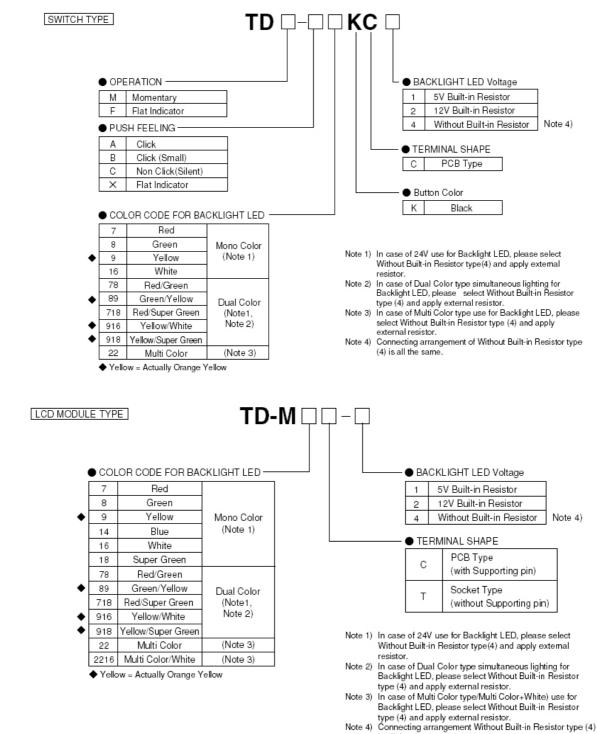
- In multi-color + W type (TD-M2216C-4), white LED is added at the L4 terminal in addition to L1, L2 and L3 terminals with multiple colors. (L4 terminal does not support color emission types other than multi-color + W type).

There is no built-in resistor for white LED. Please install a protective resistor with reference to "backlight LED setting value." - Switch types do not have the L4 terminal.

	Standard type			High	brightness	s type	Multi-color, multi-color + White			
Emission color	Red	Green	Yellow	Blue	Super	White	Red	Blue	Super	White
					Green				Green	
Setting current value (mA)	3.6	11.7	10.6	10.2	4.1	11.3	9.2	10.2	6	11.3
DC5V setting resistance value	270Ω	82Ω	100Ω	220Ω	470Ω	160Ω	370Ω	220Ω	330Ω	160Ω
DC12V setting resistance value	2.2KΩ	680Ω	750Ω	1 K Ω	2.2KΩ	750Ω	1.1KΩ	1 K Ω	1.5KΩ	750Ω
DC24V setting resistance value	5.6KΩ	1.8KΩ	1.8KΩ	2.1KΩ	5.1KΩ	1.8KΩ	2.4KΩ	2ΚΩ	3.6KΩ	1.8KΩ

Backlight LED setting value (reference)

< ORDERING CODE >



is all the same.

< Precautions before use >

- Caution is required in handling not to cause static to be applied on the main unit. 1.
- The LCD block may become damaged if excessive force is applied. Do not apply force. 2.
- Use a protective cover not to expose the display surface when using this product. 3.
- Never dissemble or modify the product. It may lead to product failure. 4.
- Due caution is required to prevent noise from entering the power supply or control lines. It may lead to display failure or 5. product damage.
- Dip soldering in solder bath is not supported by "T: For socket" in "Terminal shape code" type name specification. 6. Only manual soldering is supported. If you wish to perform dip soldering in solder bath, use "C: For printed board" or a special socket.

- 7. The polarizer on the display surface of LCD panel may be affected by organic solvents. Adhere dust using Scotch tape or wipe lightly with soft materials such as absorbent cotton if the display surface becomes soiled.
- 8. Do not drink the leaking liquid crystal if LCD panel becomes damaged. If there is contact with skin or clothes, wash off with soap and water.
- 9. Do not connect or remove the product with power supply ON. It may cause product failure.
- 10. If this product is used outside the environmental conditions for use -10° C to $+40^{\circ}$ C and 80%RH, it may facilitate liquid crystal deterioration and cause lifetime to reduce or defects. Avoid using the product outside the environmental conditions for use.
- 11. Note that due caution will be required since inputting the signals before VDD stabilizes may cause the driver IC to be damaged by floating phenomena or latch-up phenomena.
- 12. Depending on the fluctuation of VDD input power supply, there may be effects on LCD contrast. Insert a stabilization circuit for input power supply to suppress the fluctuation to about $\pm 2\%$.

<Precautions on solder work>

- Manual soldering: 350°C, within 3 seconds
- Solder bath: 260°C, within 3 seconds (Note: Terminal shape "T" types does not support use of solder bath.)

* Prevent heat from being applied to LCD display elements during soldering.

- * It cannot be immersed and washed entirely after soldering.
- * Soldering in reflow furnace is not supported.

<Precautions in storage>

- 1. Under high temperature and humidity, it may cause polarization deterioration, bubble generation or polarizer peeling. Do not use or store the product under such conditions.
- 2. Do not store the product in spaces where there may be direct sunlight or ultraviolet.
- 3. Avoid storing the product in conditions where external forces may be applied.

<Precautions on accessories>

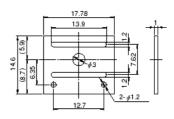
• About the spacer

- By using terminal shape "C" type, it is possible to adjust the height to 1mm 4mm.
 - * It cannot be used for terminal shape "T" for sockets.
 - * It cannot be used in combination with the socket.

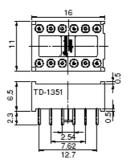
• About the socket

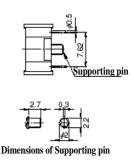
- Supported board thickness: Max 1.6mm (recommended)
- Mechanical lifetime of contact section: 100 times (minimum)

Note: Caution is required that there is no room for terminal length in the socket when board thickness is 2mm although it is possible to use. Note: Soldering in reflow furnace is not supported.



* By using the spacer, it is possible to adjust the height to 1mm – 4mm.
* It cannot be used in combination with the socket.







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